

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		10550323
	Filing Date		2005-09-23
	First Named Inventor	Roger D. Chamberlain	
	Art Unit	2434 2162	
	Examiner Name	M.T. Henning JBF	
	Attorney Docket Number	53047-57370	

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	3	SIDHU et al., "Fast Regular Expression Matching using FPGAs", IEEE Symposium on Field Programmable Custom Computing Machines (FCCM 2001), April 2001. All pages.	<input type="checkbox"/>
	4	SIDHU et al., "String Matching on Multicontext FPGAs using Self-Reconfiguration", FPGA '99: Proceedings of the 1999 ACM/SIGDA 7th International Symposium on Field Programmable Gate Arrays, February 1999, pp. 217-226	<input type="checkbox"/>
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6	TAYLOR et al., "Generalized RAD Module Interface Specification of the Field Programmable Port Extender (FPX) Version 2", Washington University, Department of Computer Science, Technical Report, January 8, 2000 . All Pages .	<input checked="" type="checkbox"/>
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